

DATA STRUCTURE FOR EFFICIENT ENQUEUEING AND DEQUEUEING

RELATED APPLICATION

This application claims priority to and the benefit of the provisional patent application entitled "A Method to Enqueue and Dequeue from a Priority Queue Efficiently," filed on May 15, 2000, and assigned serial no. 60/204,221.

FIELD OF THE INVENTION

This invention generally relates to ranked entities, and more particularly to ranked entities that are prioritized threads.

BACKGROUND OF THE INVENTION

Computer programs are commonly made up of a number of threads that are run within an operating system. Each thread typically has a priority within a range of priority levels. This enables the operating system to determine which threads should have greater priority when a number of different threads are asking to be executed at the same time. Operating systems, depending on their design, can have a large number of different priority levels. For example, some operating systems may have only eight priority levels, while others may have 256 priority levels.

Real-time operating systems are those that guarantee execution of instructions within some predetermined, worst-case time limit, or bound. Because of this bound, real-time operating systems desirably must be able to enqueue prioritized threads to and dequeue prioritized threads from a priority queue that tracks all the threads within a predetermined maximum worst case limit. This is difficult to accomplish for a real-time

skilled in the art to practice the invention. Other embodiments may be utilized, and logical, mechanical, electrical, and other changes may be made without departing from the spirit or scope of the present invention. The following detailed description is, therefore, not to be taken in a limiting sense, and the scope of the present invention is
5 defined only by the appended claims.

Data Structure

FIG. 1 is a diagram showing a preferred data structure 100 of the invention. The data structure 100 efficiently orders a number of ranked entities by using an array 102. The ranked entities can be prioritized threads, and the array 102 can be a priority queue.
10 The entities can also be software objects, components, nodes, or other constructs. The array 102 includes a number of array entries 104a, 104b, 104c, 104d, . . . , 104n. The rank of each ranked entity is selected from a number of ranks, which can be thread priorities. For example, there can be 256 ranks, where rank 1 can be considered a greater rank than rank 256. Alternatively, rank 256 can be considered greater than rank 1. The ranks are
15 distributed over the array entries 104 of the array 102, such that each array entry has a corresponding rank range. The rank distribution can be equal or unequal. For example, where there are eight array entries 104, each array entry can correspond to 32 ranks in an equal distribution over the array entries 104. The first array entry would correspond to ranks and have a rank range of 1-32, the second entry would correspond to ranks and
20 have a rank range of 33-64, and so on.

An example is used where there are 20 ranks, distributed equally over five array entries 104a, 104b, 104c, 104d, and 104n. Array entry 104a corresponds to ranks 1-4, array entry 104b corresponds to ranks 5-8, entry 104c corresponds to ranks 9-12, entry

links the entities in a first vertical direction, as indicated by the arrows 113. Each vertically linked list links the entities optionally in a second vertical direction, as indicated by the arrows 115. The data structure 100 also can include a head pointer 106 that points to one of the entities having the greatest rank that is within the horizontally linked list 103. For example, the head pointer 106 points to the entity 108, which has a rank of 2.

In summary, the data structure 100 uses the array 102 and the horizontally linked list 103 to efficiently order ranked entities. Where there are ranked entities that have equal rank, the data structure 100 also uses vertically linked lists, such as the vertically linked lists 109 and 111. Between the horizontally linked list 103, and the vertically linked lists 109 and 111, all of the ranked entities are linked into at least one of the linked lists. The next two sections of the detailed description describe how embodiments of the invention add and remove ranked entities to and from the data structure 100.

Removal of Ranked Entities

FIG. 2 is a flowchart showing how one embodiment removes a particular ranked entity from the data structure 100 of FIG. 1. Where the entity is a prioritized thread, and the array 102 of FIG. 1 is a priority queue, the removal process of FIG. 2 can be referred to as dequeuing the prioritized thread from the priority queue. The method 200 first determines whether the particular entity is in a vertically linked list, in 202. The vertically linked list can be the vertically linked list 109 or the vertically linked list 111 of FIG. 1. If the particular entity is in a vertically linked list, it is delinked from the list. This is accomplished in the vertical directions 113 and 115. In the vertical direction 113, the entity that currently points to the particular entity is repointed to the entity to which

entity is not the last entity. If the particular entity was removed from a vertically linked list in 202, then the head pointer is adjusted to point to the next entity in the list. That is, the head pointer is adjusted to point to the entity to which the particular entity had pointed in the first vertical direction 113. If the particular entry was not removed from a vertically linked list in 202, but was removed from the horizontally linked list in 204, then the head pointer is adjusted to point to the next entity in the list. That is, the head pointer is adjusted to point to the entity to which the particular entity had pointed to in the descending rank direction 105.

For example, if the entity 108 of FIG. 1 were removed, then the head pointer 106 would be adjusted to point to another entity. Because the entity 108 is in the vertically linked list 109, the head pointer 106 would be adjusted to point to the next entity in the vertically linked list 109 in the vertical direction 113, which is the entity 110. If the entity 108 were not in the vertically linked list 109, the head pointer 106 would be adjusted to point to the next entity in the horizontally linked list 103 in the descending rank direction 105, which is the entity 114. This is the case where the entities 110 and 112 are not present, such that there is no vertically linked list 109. FIG. 3 shows the data structure 100 of FIG. 1 with the entity 108 removed. With respect to the head pointer 106, it now points to the entity 110.

Addition of Ranked Entities

FIG. 4 is a flowchart showing how one embodiment adds a new ranked entity to the data structure 100 of FIG. 1. Where the new entity is a prioritized thread, and the array 102 of FIG. 1 is a priority queue, the addition process of FIG. 4 can be referred to as enqueueing the prioritized thread to the priority queue. The method 400 first

513 has a rank of 3, which is between the rank of 2 of the entity 108 and the rank of 4 of the entity 114.

With respect to the new entity 517, the new entity 517 points to the first entity in the list 109'', which is the entity 108, in the vertical direction 113. The new entity 517 points to the former last entity, which is the entity 112, in the vertical direction 115. The entity 108 now points to the new entity 517 in the vertical direction 115. The entity 112 now points to the new entity 517 in the vertical direction 113. With respect to the new entity 515, the new entity 515 points to the first entry in the list 519, which is the entity 114, in both the vertical directions 113 and 115. Similarly, the entity 114 points to the new entity 515 in both the vertical directions 113 and 115. With respect to the new entity 513, the entity 108 now points to the new entity 513 in the descending rank order direction 105, and the new entity 513 points to the entity 114 in the descending rank order direction 105. The entity 114 now points to the new entity 513 in the ascending rank order direction 107, and the new entity 513 points to the entity 108 in the ascending rank order direction 107.

Example Computerized Device

The invention can be implemented within a computerized environment having one or more computerized devices. The diagram of FIG. 6 shows an example computerized device 600. The example computerized device 600 can be, for example, a desktop computer, a laptop computer, or a personal digital assistant (PDA). The invention may be practiced with other computer system configurations as well, including multiprocessor systems, microprocessor-based or programmable consumer electronics, network computers, minicomputers, and mainframe computers. The invention may be

practiced in distributed computing environments where tasks are performed by remote processing devices that are linked through a communications network.

The device 600 includes one or more of the following components: processor(s) 602, memory 604, storage 606, a communications component 608, input device(s) 610, a display 612, and output device(s) 614. For a particular instantiation of the device 600, one or more of these components may not be present. For example, a PDA may not have any output device(s) 614. The description of the device 600 is to be used as an overview of the types of components that typically reside within such a device, and is not meant as a limiting or exhaustive description.

The processor(s) 602 may include a single central-processing unit (CPU), or a plurality of processing units, commonly referred to as a parallel processing environment. The memory 604 may include read-only memory (ROM) and/or random-access memory (RAM). The storage 606 may be any type of storage, such as fixed-media storage devices and removable-media storage devices. Examples of the former include hard disk drives, and flash or other non-volatile memory. Examples of the latter include tape drives, optical drives like CD-ROM drives, and floppy disk drives. The storage devices and their associated machine-readable media provide non-volatile storage of machine-readable instructions, data structures, program modules, and other data. Any type of machine-readable media that can store data and that is accessible by a computer can be used.

The device 600 may operate in a network environment. Examples of networks include the Internet, intranets, extranets, local-area networks (LAN's), and wide-area networks (WAN's). The device 600 may include a communications component 608,

which can be present in or attached to the device 600. The component 608 may be one or more of a network card, an Ethernet card, an analog modem, a cable modem, a digital subscriber loop (DSL) modem, and an Integrated Services Digital Network (ISDN) adapter. The input device(s) 610 are the mechanisms by which a user provides input to the device 600. Such device(s) 610 can include keyboards, pointing devices, microphones, joysticks, game pads, and scanners. The display 612 is how the device 600 typically shows output to the user. The display 612 can include cathode-ray tube (CRT) display devices and flat-panel display (FPD) display devices. The device 600 may provide output to the user via other output device(s) 614. The output device(s) 614 can include speakers, printers, and other types of devices.

The methods that have been described can be computer-implemented on the device 600. A computer-implemented method is desirably realized at least in part as one or more programs running on a computer. The programs can be executed from a machine-readable medium such as a memory by a processor of a computer. The programs are desirably storable on a machine-readable medium, such as a floppy disk or a CD-ROM, for distribution and installation and execution on another computer. The program or programs can be a part of a computer system, a computer, or a computerized device. Furthermore, data structures as have been described can be stored on a machine-readable medium.

Conclusion

It is noted that, although specific embodiments have been illustrated and described herein, it will be appreciated by those of ordinary skill in the art that any arrangement that is calculated to achieve the same purpose may be substituted for the

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